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FFSB20120A

Silicon Carbide Schottky Diode

1200 V, 20 A

Description

Silicon Carbide (SiC) Schottky Diodes use a completely new technology that provides superior switching performance and higher reliability compared to Silicon. No reverse recovery current, temperature independent switching characteristics, and excellent thermal performance sets Silicon Carbide as the next generation of power semiconductor. System benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size & cost.

Features

- Max Junction Temperature 175°C
- Avalanche Rated 200 mJ
- High Surge Current Capacity
- Positive Temperature Coefficient
- Ease of Paralleling
- No Reverse Recovery / No Forward Recovery
- These Devices are Pb−Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

- General Purpose
- SMPS, Solar Inverter, UPS
- Power Switching Circuits

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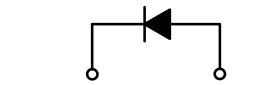
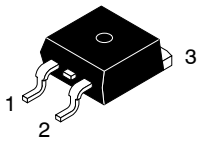
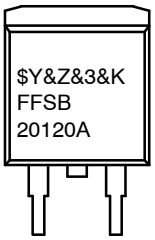


Schottky Diode



D²PAK−3 (TO−263, 3−LEAD)
CASE 418AJ

MARKING DIAGRAM



$Y	= ON Semiconductor Logo
&Z	= Assembly Plant Code
&3	= Numeric Date Code
&K	= Lot Code
FFSB20120A	= Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

ABSOLUTE MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

Symbol	Parameter		Value	Unit
V_{RRM}	Peak Repetitive Reverse Voltage		1200	V
E_{AS}	Single Pulse Avalanche Energy (Note 1)		200	mJ
I_F	Continuous Rectified Forward Current @ T_C < 157°C		20	A
	Continuous Rectified Forward Current @ T_C < 135°C		32	
$I_{F, Max}$	Non-Repetitive Peak Forward Surge Current	T_C = 25°C, 10 μs	1190	A
		T_C = 150°C, 10 μs	990	A
$I_{F,SM}$	Non-Repetitive Forward Surge Current	Half-Sine Pulse, t_p = 8.3 ms	135	A
$I_{F,RM}$	Repetitive Forward Surge Current	Half-Sine Pulse, t_p = 8.3 ms	74	A
Ptot	Power Dissipation	T_C = 25°C	333	W
		T_C = 150°C	55	W
T_J, T_{STG}	Operating and Storage Temperature Range		−55 to +175	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. E_{AS} of 200 mJ is based on starting T_J = 25°C, L = 0.5 mH, I_{AS} = 29 A, V = 50 V.

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max	0.45	°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
V_F	Forward Voltage	I_F = 20 A, T_C = 25°C	–	1.45	1.75	V
		I_F = 20 A, T_C = 125°C	–	1.7	2.0	
		I_F = 20 A, T_C = 175°C	–	2.0	2.4	
I_R	Reverse Current	V_R = 1200 V, T_C = 25°C	–	–	200	μA
		V_R = 1200 V, T_C = 125°C	–	–	300	
		V_R = 1200 V, T_C = 175°C	–	–	400	
Q_C	Total Capacitive Charge	V = 800 V	–	120	–	nC
C	Total Capacitance	V_R = 1 V, f = 100 kHz	–	1220	–	pF
		V_R = 400 V, f = 100 kHz	–	111	–	
		V_R = 800 V, f = 100 kHz	–	88	–	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

ORDERING INFORMATION

Part Number	Top Marking	Package	Shipping
FFSB20120A	FFSB20120A	D^2PAK−3 (Pb-Free / Halogen Free)	800 / Tape & Reel

TYPICAL CHARACTERISTICS

(T_J = 25°C unless otherwise noted)

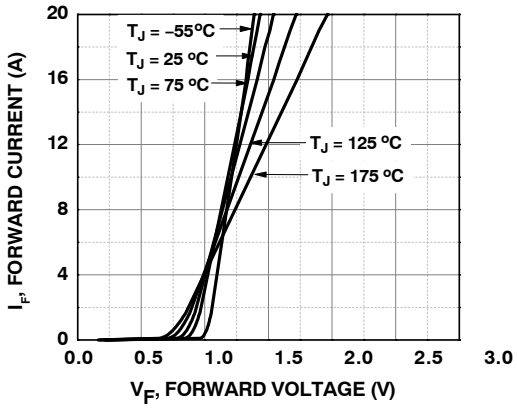


Figure 1. Forward Characteristics

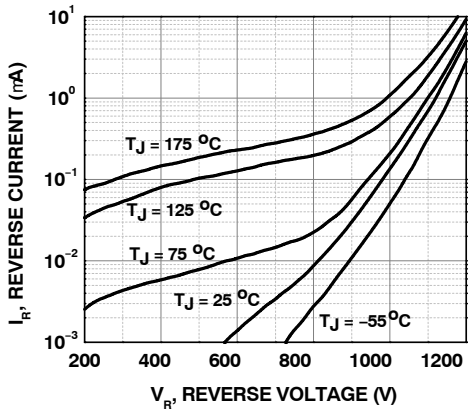


Figure 2. Reverse Characteristics

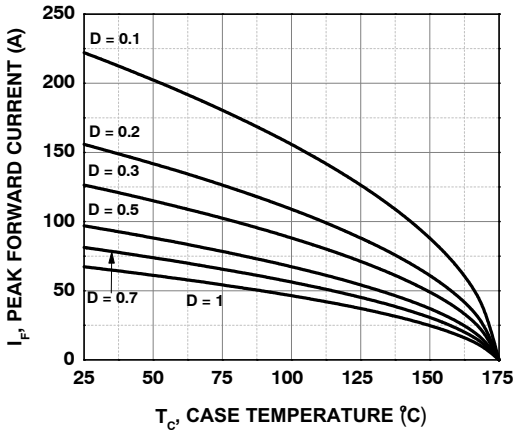


Figure 3. Current Derating

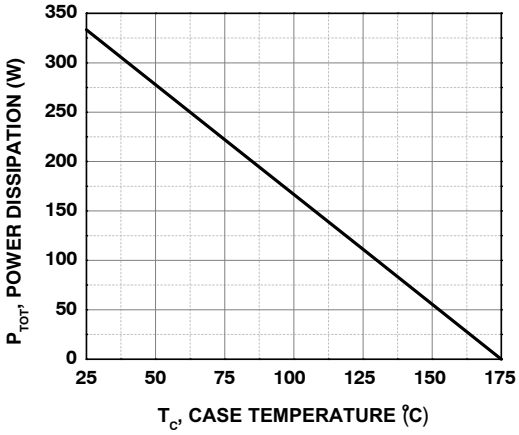


Figure 4. Power Derating

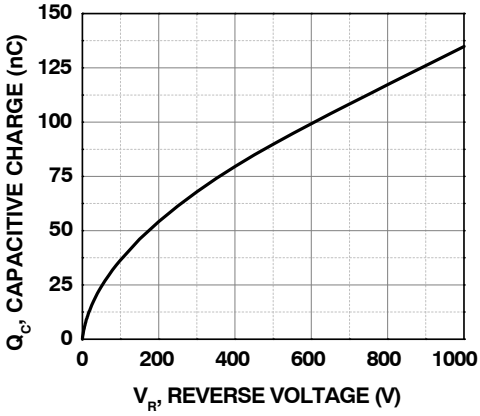


Figure 5. Capacitive Charge vs. Reverse Voltage

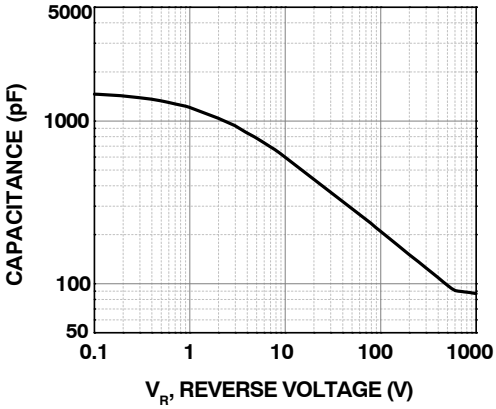


Figure 6. Capacitance vs. Reverse Voltage

TYPICAL CHARACTERISTICS

(T_J = 25°C unless otherwise noted)

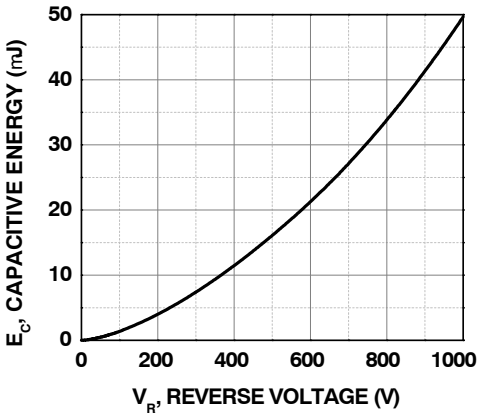


Figure 7. Capacitance Stored Energy

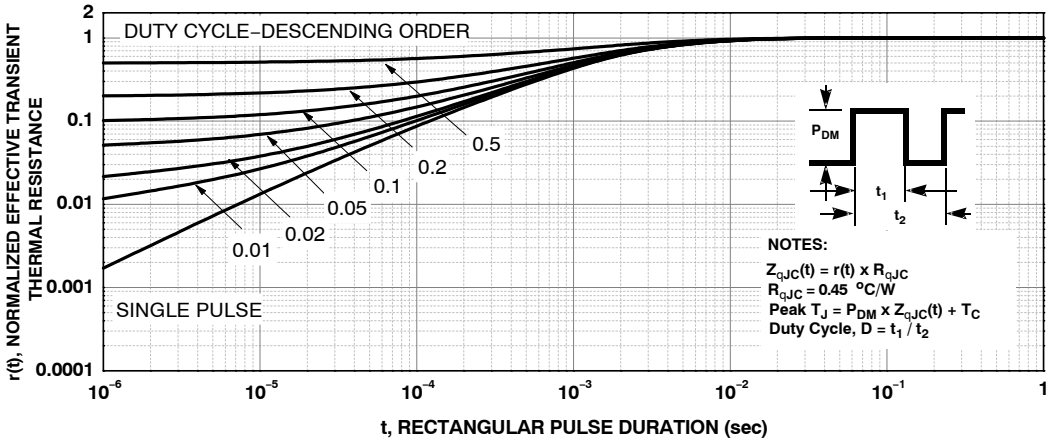


Figure 8. Junction-to-Case Transient Thermal Response Curve

TEST CIRCUIT AND WAVEFORMS

L = 0.5 mH
R < 0.1 Ω
V_{DD} = 50 V
EAVL = 1/2LI2 [$V_{R(AVL)}$ / ($V_{R(AVL)}$ – V_{DD})]
Q1 = IGBT (BV_{CES} > DUT $V_{R(AVL)}$)

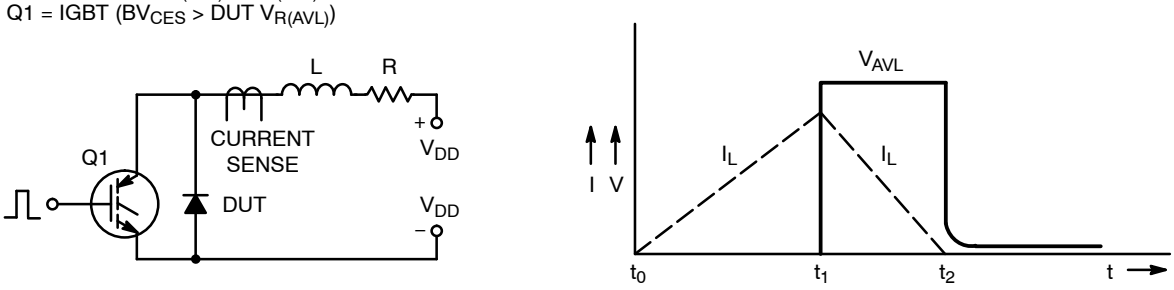
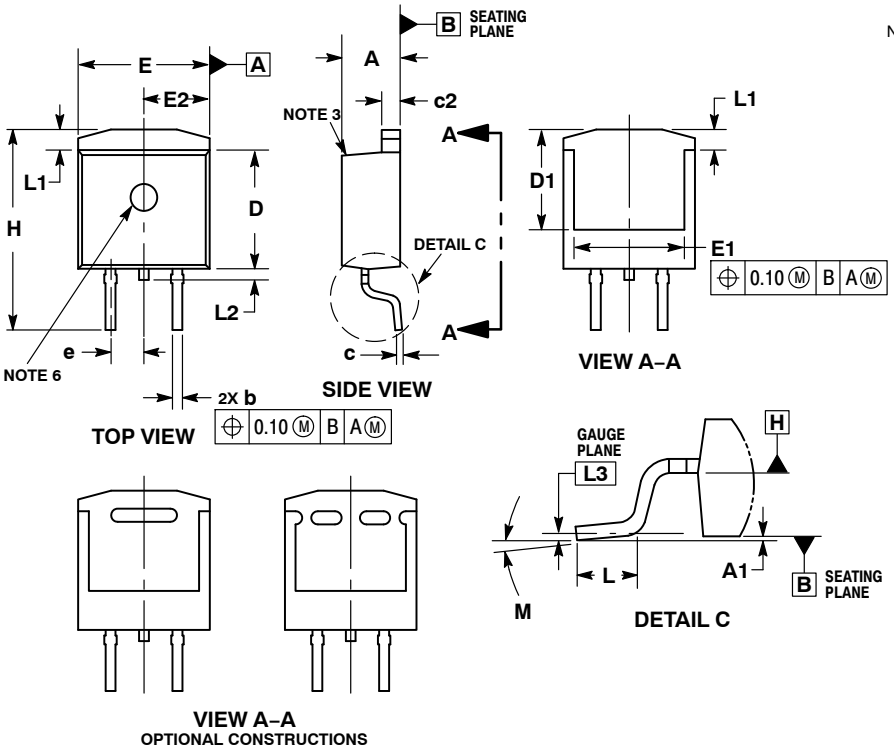


Figure 9. Unclamped Inductive Switching Test Circuit & Waveform

PACKAGE DIMENSIONS

D²PAK−3 (TO−263, 3−LEAD)
CASE 418AJ
ISSUE B

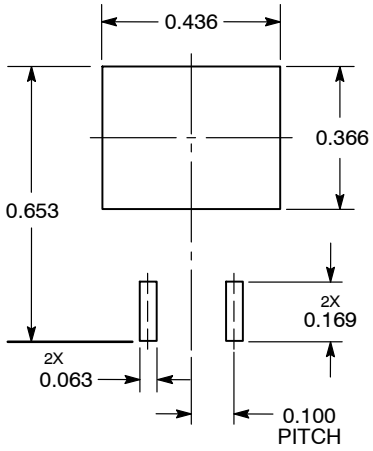


VIEW A−A
OPTIONAL CONSTRUCTIONS

NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: INCHES.
3. CHAMFER OPTIONAL
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.005 PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY AT DATUM H.
5. THERMAL PAD CONTOUR IS OPTIONAL WITHIN DIMENSIONS E, L1, D1 AND E1.
6. OPTIONAL MOLD FEATURE

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
A	0.160	0.190	4.06	4.83
A1	0.000	0.010	0.00	0.25
b	0.020	0.039	0.51	0.99
c	0.012	0.029	0.30	0.74
c2	0.045	0.065	1.14	1.65
D	0.330	0.380	8.38	9.65
D1	0.260	----	6.60	----
E	0.380	0.420	9.65	10.67
E1	0.245	----	6.22	----
e	0.100 BSC		2.54 BSC	
H	0.575	0.625	14.60	15.88
L	0.070	0.110	1.78	2.79
L1	----	0.066	----	1.68
L2	----	0.070	----	1.78
L3	0.010 BSC		0.25 BSC	
M	0°	8°	0°	8°

RECOMMENDED SOLDERING FOOTPRINT*



DIMENSIONS: INCHES

*For additional information on our Pb−Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:
Literature Distribution Center for ON Semiconductor
19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA
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Fax: 303−675−2176 or 800−344−3867 Toll Free USA/Canada
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