

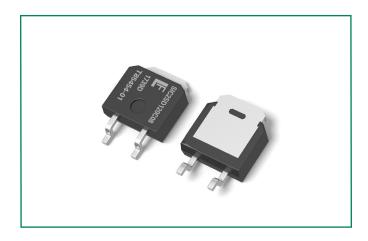
# GEN2 SiC Schottky Diode LSIC2SD120C08, 1200 V, 8 A, TO-252-2L (DPAK)

# LSIC2SD120C08









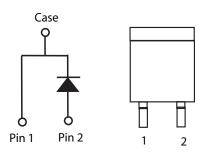
### **Description**

This series of silicon carbide (SiC) Schottky diodes has negligible reverse recovery current, high surge capability, and a maximum operating junction temperature of 175 °C. These diodes series are ideal for applications where improvements in efficiency, reliability, and thermal management are desired.

#### **Features**

- Positive temperature coefficient for safe operation and ease of paralleling
- 175 °C maximum operating junction temperature
- Excellent surge capability
- Extremely fast, temperature-independent switching behavior
- Dramatically reduced switching losses compared to Si bipolar diodes

## Circuit Diagram TO-252-2L ( DPAK )



### **Applications**

- Boost diodes in PFC or DC/DC stages
- Switch-mode power supplies
- Uninterruptible power supplies
- Solar inverters
- Industrial motor drives
- EV charging stations

#### **Environmental**

- Littelfuse "RoHS" logo = RoHS RoHS conform
- Littelfuse "HF" logo = **HF**Halogen Free
- Littelfuse "PB-free" logo = Pb-free lead plating

### **Maximum Ratings**

| Characteristics                      | Symbol                | Conditions  | Value      | Unit |  |
|--------------------------------------|-----------------------|---|------------|------|--|
| Repetitive Peak Reverse Voltage      | $V_{_{\mathrm{RRM}}}$ | -   | 1200       | V    |  |
| DC Blocking Voltage                  | V <sub>R</sub>        | T <sub>j</sub> = 25 °C                                    | 1200       | V    |  |
|                                      |                       | T <sub>c</sub> = 25 °C                                    | 24.5       | А    |  |
| Continuous Forward Current           | l <sub>F</sub>        | T <sub>C</sub> = 135 °C                                   | 12         |      |  |
|                                      |                       | T <sub>C</sub> = 154 °C                                   | 8          |      |  |
| Non-Repetitive Forward Surge Current | I <sub>FSM</sub>      | $T_{\rm C} = 25$ °C, $T_{\rm P} = 10$ ms, Half sine pulse | 65         | А    |  |
|                                      | P <sub>Tot</sub>      | T <sub>C</sub> = 25 °C                                    | 125        | W    |  |
| Power Dissipation                    |                       | T <sub>C</sub> = 110 °C                                   | 54         | VV   |  |
| Operating Junction Temperature       | T <sub>J</sub>        | -   | -55 to 175 | °C   |  |
| Storage Temperature                  | T <sub>stg</sub>      | -   | -55 to 150 | °C   |  |
| Soldering Temperature                | T <sub>sold</sub>     | -   | 260        | °C   |  |



### **Electrical Characteristics**

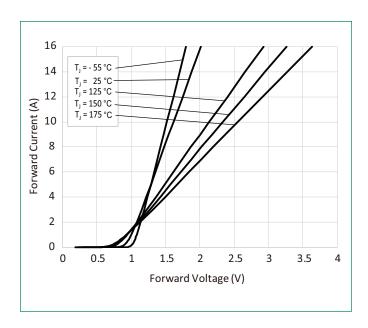
| Characteristics         |                | Conditions  | Value |      |      |      |  |
|-------------------------|----------------|---|-------|------|------|------|--|
|                         | Symbol         |   | Min.  | Тур. | Max. | Unit |  |
| Forward Voltage         | V <sub>F</sub> | I <sub>F</sub> = 8 A, T <sub>J</sub> = 25 °C              | -     | 1.5  | 1.8  | V    |  |
|                         |                | I <sub>F</sub> = 8 A, T <sub>J</sub> = 175 °C             | -     | 2.2  | -    |      |  |
| Reverse Current         | I <sub>R</sub> | V <sub>R</sub> = 1200 V , T <sub>J</sub> = 25 °C          | -     | <1   | 100  | μА   |  |
|                         |                | V <sub>R</sub> = 1200 V , T <sub>J</sub> = 175 °C         | -     | 10   |      |      |  |
| Total Capacitance       | С              | V <sub>R</sub> = 1 V, f =1 MHz                            | -     | 454  | -    | pF   |  |
|                         |                | V <sub>R</sub> = 400 V, f = 1 MHz                         | -     | 45   | -    |      |  |
|                         |                | V <sub>R</sub> = 800 V, f = 1 MHz                         | -     | 33   | -    |      |  |
| Total Capacitive Charge | O <sub>c</sub> | $V_{R} = 800 \text{ V},  Q_{C} = \int_{0}^{V_{R}} C(V)dV$ | -     | 47   | -    | nC   |  |

Footnote: T<sub>1</sub> = +25 °C unless otherwise specified

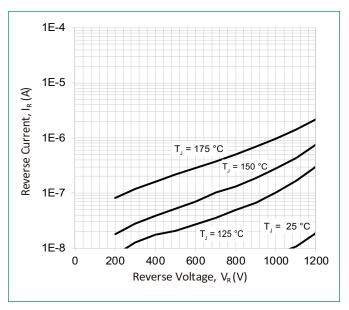
## **Thermal Characteristics**

| Characteristics    |                   |      | Value |      |      |      |
|--------------------|-------------------|------|-------|------|------|------|
|                    | Symbol Conditions | Min. | Тур.  | Max. | Unit |      |
| Thermal Resistance | R <sub>eic</sub>  | -    | -     | 1.2  | -    | °C/W |

**Figure 1: Typical Foward Characteristics** 



**Figure 2: Typical Reverse Characteristics** 





**Figure 3: Power Derating** 

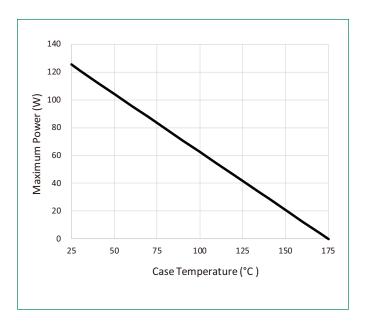


Figure 4: Current Derating

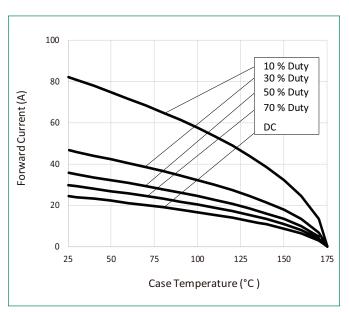


Figure 5: Capacitance vs. Reverse Voltage

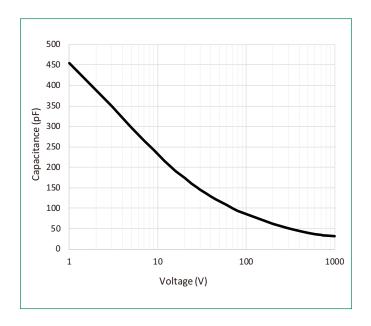


Figure 6: Capacitive Charge vs. Reverse Voltage

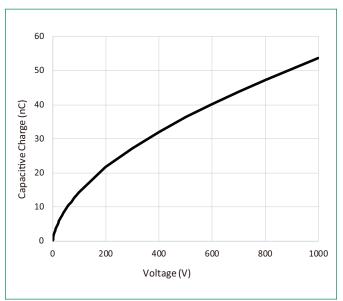




Figure 7: Stored Energy vs. Reverse Voltage

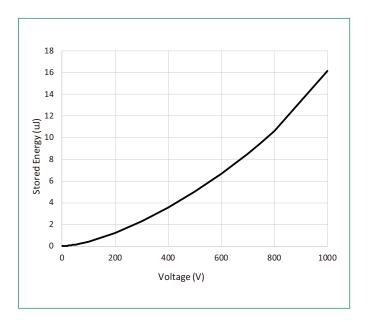
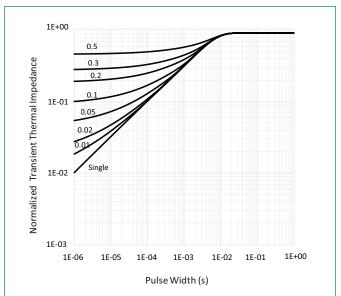
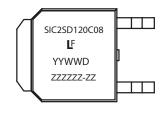


Figure 8: Transient Thermal Impedance



## Part Numbering and Marking System



| SIC | = SiC Diode               |
|-----|---------------------------|
| 2   | = Gen2                    |
| SD  | = Schottky Diode          |
| 120 | = Voltage Rating (1200 V) |
| С   | = TO-252 2-Lead Package   |
| 80  | = Current Rating ( 8 A)   |
| ΥY  | = Year                    |
| WW  | = Week                    |

ZZZZZZ-ZZ = Lot Number

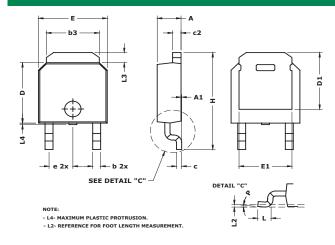
= Special code (fixed)

D

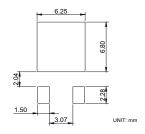
## **Packing Options**

| Part Number   | Part Number Marking |               | M.O.Q |
|---------------|---------------------|---------------|-------|
| LSIC2SD120C08 | SIC2SD120C08        | Tape and Reel | 2500  |

#### **Dimensions TO-252-2L (DPAK)**

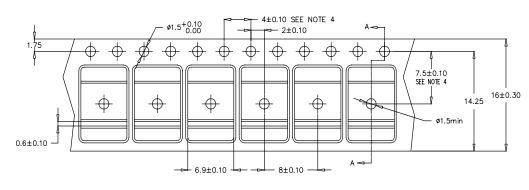


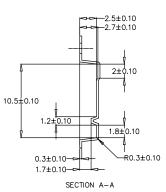
#### Recommended Solder Pattern Layout



| Cumphial | Inches    |       |       | Millimeters |      |       |  |
|----------|-----------|-------|-------|-------------|------|-------|--|
| Symbol   | Min       | Nom   | Max   | Min         | Nom  | Max   |  |
| Α        | 0.085     | 0.090 | 0.095 | 2.16        | 2.29 | 2.41  |  |
| A1       | 0         | 0.003 | 0.005 | 0           | 0.08 | 0.13  |  |
| b        | 0.025     | 0.030 | 0.035 | 0.64        | 0.76 | 0.89  |  |
| b3       | 0.195     | 0.200 | 0.215 | 4.95        | 5.08 | 5.46  |  |
| С        | 0.018     | 0.020 | 0.024 | 0.46        | 0.51 | 0.61  |  |
| C2       | 0.018     | 0.032 | 0.035 | 0.46        | 0.81 | 0.89  |  |
| D        | 0.235     | 0.240 | 0.245 | 5.97        | 6.10 | 6.22  |  |
| D1       | 0.205     | -     | -     | 5.21        | -    | -     |  |
| Е        | 0.250     | 0.260 | 0.265 | 6.35        | 6.60 | 6.73  |  |
| E1       | 0.170     | -     | -     | 4.32        | -    | -     |  |
| е        | 0.090 BSC |       |       | 2.29 BSC    |      |       |  |
| Н        | 0.370     | 0.387 | 0.410 | 9.40        | 9.83 | 10.41 |  |
| L        | 0.040     | 0.045 | 0.050 | 1.02        | 1.14 | 1.27  |  |
| L2       | 0.010 BSC |       |       | 0.25 BSC    |      |       |  |
| L3       | 0.035     | -     | 0.050 | 0.89        | -    | 1.27  |  |
| L4       | 0         | -     | 0.006 | 0           | -    | 0.15  |  |
| Р        | 0°        | -     | 8°    | 0°          | -    | 8°    |  |

## Carrier Tape & Reel Specification TO-252-2L (DPAK)





- Material: Black Conductive Polysterene
  10 sprocket hole pitch cumulative tolerance ± 0.20
  3. Camber not to exceed 1 mm in 100 mm.
  4 Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.
  5. Device orientation: TRL (leads perpendicular to the sprocket)
- 6. General tolerance is  $\pm$  0.10 mm unless otherwise specified.

#### COVER TAPE SPECS:

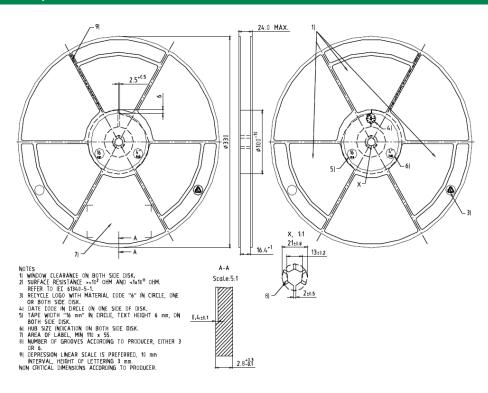
Width : 13.5 mm

: less than 1.2x10<sup>12</sup> ohms/square Base Material Transparent polyester, static dissipative

: Polyethylene Adhesive Laver Total Thickness : 60 Micron Tensile Strength :  $4-6 \text{ kg/mm}^2$ : 91% Elongation Tearing Strength : 11 kg/mm² : 2 years Shelf life



#### Carrier Tape & Reel Specification TO-252-2L (DPAK)



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